

Howard L. Bernstein T 202-663-7937 hbernstein@sughrue.com

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10/803983 10/803983 031904

2100 Pennsylvania Avenue, NW Washington, DC 20037-3213 T 202.293.7066 F 202.293.7869

401 Castro Stréet Mountain View, CA 94041-2007 T 650.625.8100 F 650.625.8110

> AlG Bldg. 14F 1-1-3 Marunouchi Chiyoda-ky Toyko 100-0005 Japan T 03.5220.0200 F 03.5220.0222

> > www.sughrue.com

MAIL STOP PATENT APPLICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Da.

Application of Mitsuhiro SHIMAMOTO

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE Assignee: NEC ELECTRONICS CORPORATION

Our Ref.: Q80572

Dear Sir:

Attached hereto is the application identified above comprising 14 sheets of the specification, including the claims and abstract, 4 sheets of drawings, a copy of the executed Assignment and PTO 1595 form, and a copy of the executed Declaration and Power of Attorney. Also enclosed is an Information Disclosure Statement and PTO/SB/08 A & B (modified) with reference.

The Government filing fee is calculated as follows:

Total claims	5 - 20	=	X	\$18.00	=	\$.00
Independent claims	1 - 3	=	x	\$86.00	=	\$.00
Base Fee						\$770.00
TOTAL FILING FEE						\$770.00
Recordation of Assignment						\$40.00
TOTAL FEE						\$810.00

Checks for the statutory filing fee of \$770.00 and Assignment recordation fee of \$40.00 are attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached.



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Priority is claimed from:

Country

Application No

Filing Date

Japan

2003-091782

March 28, 2003

The priority document is enclosed herewith.

Respectfully submitted, SUGHRUE MION, PLLC

Attorneys for Applicant

By:

Howard L. Bernstein

Registration No. 25,665

SUGHRUE MION, PLLC Telephone: (202) 293-7060 Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

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